

If you are reading this, most probably, you are about to build Erica Synths DIY Polivoks-inspired Envelope generator. This module is 30mm deep, skiff friendly, has solid mechanical construction and doesn't require wiring. The core of the Envelope Generator is borrowed from famous Russian synth Polivoks, but we significantly updated schematics:

- 1) the original Polivoks has negative gate, therefore we made the EG work from eurorack standard positive gates,
- 2) we adjusted envelope amplitude for eurorack modular requirements,
- 3) we introduced inverted envelope output,
- 4) we updated looping feature (looping on gate on),
- 5) most importantly, we added Gate Delay feature – the module outputs delayed gate signal after Attack stage. Chain up several (4-5) Envelope Generators, each with delayed gate to create evolving polyphonic synth-like pads!

The DIY Envelope kit comes in three versions:

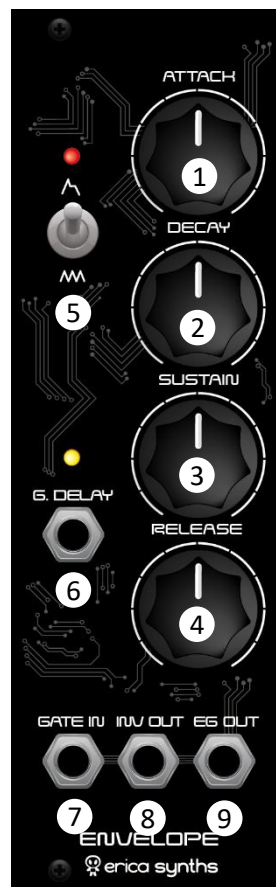
- 1) PCB,
- 2) PCB + panel,
- 3) Full kit.

## FEATURES:

- Full ADSR envelope
- Direct and inverted envelope outputs
- Gated and independent looping modes
- Gate delay (after Attack stage) output
- Skiff-friendly design

## SPECIFICATIONS:

- |                      |                      |
|----------------------|----------------------|
| • Envelope amplitude | 0...10V              |
| • Attack time        | 0...3"               |
| • Decay time         | 0...12"              |
| • Sustain level      | 0...10V              |
| • Release time       | 0...12"              |
| • Panel width        | 8HP                  |
| • Module depth       | 30mm                 |
| • Power consumption  | 20mA@+12V, 10mA@-12V |



- 1 Adjust Attack time from 0 to 3"!
- 2 Adjust Decay time from 0 to 12"!
- 3 Adjust Sustain level from 0 to 10V!
- 4 Adjust Release time from 0 to 12"!
- 5 Select between normal ADSR and looping AR mode! If patch cable is patched in Gate jack, looping will occur only if the Gate signal is present.
- 6 This is Gate Delay output. High Gate signal appears here after the Attack stage.
- 7 This is Gate input.
- 8 This is inverted envelope output.
- 9 This is main envelope output.